

**DEPARTMENT OF COMPUTER SCIENCE & APPLICATIONS
CHAUDHARYDEVILALUNIVERSITY, SIRSA (HARYANA)
(Established by State Legislature Act 9 of 2003)**

Name of the Faculty

Faculty of Physical Sciences

Faculty Information

Dean: Prof. Dilbag Singh

Name of the Department

Department of Computer Science & Applications

Chairperson: Prof. Vikram Singh

Department Profile

The Department of Computer Science and Applications had been established in the year 2000 (since the times of Ch. Devi Lal PGRC, Sirsa). The department is under the Faculty of Physical Sciences. In the department three regular teachers have been appointed i.e. One Professor, One Associate Professor and One Assistant Professor. One Assistant Professor on Deputation from GJU S&T Hisar. Besides that 13 number of Assistant Professor (Contractual) have also been appointed for smooth conduct of classes and other academic and related activities of the Department. The Department office and labs are managed by one regular non-teaching staff (a Jr. Scale Stenographer) and 4 numbers of non-teaching staff through outsourcing agency (2 numbers of Lab attendants, one peon and one sweeper). The Departments runs the following four academic/professional programmes Ph.D, M.Tech (Full Time), M.Tech (Part Time) and MCA. The details of the seats in various courses are as under:

Ph.D Computer Science seats vary from session to session

MCA	60
M.Tech (Full Time)	40
M.Tech (Part Time)	40

The Department has been allocated 6 lecture theatres for conduct of theory classes, one room equipped with an LCD projector for conduct of seminar classes and three computer laboratories (with wi-fi facility).

Course Offered : **Ph.D, M.Tech (FT), M.Tech (PT), MCA**

Sr. No.	Course	Objective,	Minimum duration	Intake	Admission Procedure	Total Annual Fee
1.	Ph.D	To expand the research arena in the field of computer science	2 years after the date of regn	Not fixed	As per Ph.D ordinance	--
2.	M.Tech (FT)	To prepare competent teachers/researchers in the stream of computer science and engineering	2 years	40	GATE, Merit of the qualifying examination plus weightage (s) admissible	Rs. 25000/ - per year
3.	M.Tech (PT)	To provide (the working persons an) to continue and further their educational endeavours	3 years	40	GATE, Merit of the qualifying examination plus weightage (s) admissible	Rs. 33000/ - per year
4.	MCA	To prepare software engineers to cater to the needs of IT and software development hours.	3 years	60	OLET, Merit of the qualifying examination plus weightage (s) admissible	Rs. 13000/ - per year

Faculty Information

Name	Qualification	Designation	Specialization
Dr. Vikram Singh	Ph.D	Professor	OS, Data Mining Microprocessor Simulation and Modeling
Dr. Dilbag Singh	Ph.D	Associate	Computer Arch.

		Professor	Database Simulation
Dr. Harish Kumar	Ph.D	Assistant Professor	Software Reuse, Data Mining Swarm Intelligence

Facilities

Internet facilities for staff and students: Secure Wi-Fi Campus

Total number of class rooms: 06

Students' laboratories: 03

Seminar Room 01